# Oracle

## Exam 1z0-489

## SPARC M6-32 and SPARC M5-32 Servers Installation Essentials

**Version: 7.0**

**[ Total Questions:   79 ]**

**Question No : 1**

Which three statements are correct descriptions of SPARC M5-32 server design? (DCU = Domain Configuration Unit, CMU = CPU Memory Board Unit, IOU = I/O Switch Board)

**A.** In a DCU that has 2x CMU, each CMU has 2x CPU, and each CPU has two PCIe root complexes; so you can access only eight PCIe slots (that is, 2x2x2) in that IOU.
**B.** In a DCU that has 2x CMU, you need to install in position CMUO and CMU3 so that the root

complexes of the two CPUs on CMUO can connect to IOBO and link to PCIe slots 1-8; and CMU3 can connect to IOB1 and link to PCIe slot9-l6.
**C.** In a DCU that has 4x CMU, CMUO and CMU1 are connected to IOBO, and also linked to PCIe 1-8,but root complexes can work as fail over partners for high availability (the same applies to cmiw and CMU3 that connect to IOB1).
**D.** EMS modules also need a PCIe root complex to control.
**E.** In a DCU that has 2x CMU, installing in slots 0 and 1 is invalid.

**Answer: A,C,E**
Reference:

http://www.oracle.com/technetwork/server-storage/sun-sparc-enterprise/documentation/o13-056-m5-domaining-1991544.pdf

**Question No : 2**

To assist with remote diagnosis on the SPARC M5-32 platform, customer are asked to run the snapshot command. When collecting a snapshot on an Active SP, which user role is required for the user account to collect the snapshot?

**A.** a
**B.** u
**C.** c
**D.** r
**E.** o

**Answer: D**

**Question No : 3**

Which component is a building block of physical domains (PDomainsorPDoms)?

**A.** CMU
**B.** SPP
**C.** IOU
**D.** DCU

**Answer: C**
Reference:

http://docs.oracle.com/cd/E50790_01/doc/doc.121/e50471/snapshot.htm

**Question No : 4**

The SPARC M5-32 server is well –suited for many workload requirements. Which requirement does the SPARC M5-32 server best fulfill?

**A.** business-critical workloads that scale up to 32 cores
**B.** applications that need fast and reliable virtualization technologies with no extra cost
**C.** workloads that need encryption without sacrificing application performance
**D.** consolidation of mission-critical workloads, where each needs physical isolation domains (PDoms)

**Answer: D**

**Question No : 5**

You have powered-on HOST2 and want to verify its current status. Which two commands would give that output?

**A.** show /Servers/PDomains/PDoraain_2/HOST status
**B.** show /Servers/PDomains/PDomain^l/HOST status
**C.** show status /Servers/PDomains /PDomain_2/H0ST
**D.** show /Servers/PDomains /PDomain 2/HOST status detail

**Answer: A,D**
Reference:

http://docs.oracle.com/cd/E50790_01/doc/doc.121/e50471/snapshot.htm

## Question No : 6

You need to run a snapshot to collect SP data. Which two options you can dump the snapshot to?

**A.** HTTP
**B.** SFTP
**C.** HTTPS
**D.** FTP

**Answer: B,D**
Reference:

https://docs.oracle.com/cd/E20815_01/html/E20894/gjfxt.html

## Question No : 7

A customer has a newly installed SPARC M5-32 platform. After using the HS processes, the SPs are now configured on their network, and the customer wants to configure the PDomains. Which two options represent choices that the customer has to set up the platform out of the box?

**A.** accessing a Solaris domain on another host and using ipmi tool to assign components to PDomains
**B.** using the Oracle ILOM BUI to assign components to PDomains
**C.** using Oracle Ops Center to assign components to PDomains
**D.** using the service processor ILOM command-line interface to assign components to PDomains

**Answer: B,D**
Reference:

https://docs.oracle.com/cd/E24355_01/html/E41214/z40000fb1422285.html

## Question No : 8

In order to cool SPARC M5-32 servers properly, it is necessary to ensure adequate airflow. Which three statements are correct?

**A.** Allow a minimum clearance of 36 inches (914 mm) at the front and the rear of the server for ventilation.
**B.** If the server is not completely populated with components, cover the empty sections with filler panels.
**C.** Manage cables to minimize interference with the server exhaust.
**D.** It is strongly recommended that you use cooling by ceiling vents because airflow is stronger.
**E.** It is strongly recommended that you use cooling by a raised floor with perforated tiles in front of the server to supply cold air, because airflow is more evenly distributed.

**Answer: B,D,E**

## Question No : 9

Using the SP ILOM commands to list properties, which command would you use to find out the mapping for all CMU boards in physical Domain 1 to the CPUs?

**A.** show /Servers/Pdomains/Pdomain.I/System/Processors -level 3 location
**B.** show / Servers/Pdomains/Pdomain_l/DCUs -level 3 location
**C.** show /Servers/Pdomains/Pdomain_l/SP/Processors -level 2 location f
**D.** show /System/Processors/C PUs -level 2 location

**Answer: A**
Reference:

https://docs.oracle.com/cd/E24355_01/pdf/E41218.pdf

## Question No : 10

Before performing a firmware upgrade, you must first check the keyswitch property for each host. What must it be set to for the firmware upgrade to work?

**A.** Normal
**B.** Locked
**C.** Diag
**D.** Open

**Answer: D**

### Question No : 11

You have performed the initial installation of Solaris 11 on your SPARC M5-32 system. You notice thatsome packages, like SunVTS, are not currently installed. What is the reason?

**A.** There was an error in the Oracle Solaris 11 installation process. You must re-install.
**B.** SunVTS needs to be downloaded separately from http://downloads.oracle.com.
**C.** After the system has been installed and rebooted, SunVTS and other packages can be added as described in the Solaris 11 EIS checklist.
**D.** SunVTS is no longer supported on Oracle Solaris 11.

**Answer: B**
Reference:

http://docs.oracle.com/cd/E24355_01/html/E41214/z40001331392991.html

### Question No : 12

Which statement is correct?

**A.** The SPARC M5-32 processor has 16 cores, 48MB L3$; T5 processor ha 16 cores, 8MB L3$
**B.** The SPARC M5-32 processor has 6 cores, 48MB L3$; T5 processor ha 16 cores, 8MB L3$
**C.** The SPARC M5-32 processor has 16 cores, 24MB L3$; T5 processor ha 16 cores, 8MB L3$
**D.** The SPARC M5-32 processor has 6 cores, 24MB L3$; T5 processor ha 16 cores, 48MB L3$

**Answer: B**
Reference:

http://docs.oracle.com/cd/E24355_01/html/E41214/z40001331392991.html

## Question No : 13

In order for ASR to work on the system, what must the customer have configured?

**A.** ASR Host
**B.** ASR Client system
**C.** ASR Manager system
**D.** NTP

**Answer: C**
Reference:

https://docs.oracle.com/cd/E37710_01/install.41/e18475/ch1_asr_overview.htm

## Question No : 14

Your customer says that they have some specific requirements they would like to address in the boot disk layout for their system. In which Enterprise Installation Standars (EIS) document would you note this?

**A.** Site Audit Report
**B.** Installation Configuration Plan
**C.** Test Procedures Plan
**D.** Project Schedule
**E.** Operational Handover Document

**Answer: B**

## Question No : 15

Which three options are architectural features that you find in the SPARC M5-32 processor?

**A.** 16x S3 cores of 3.6 Ghz
**B.** Shared 48 MB L3$
**C.** 2x memory controllers
**D.** 2x PCI-e Gen3 root complex
**E.** 16-way 256 KB L2$

**Answer: A,B,D**

### Question No : 16

Which three statements are true about the newest clock board redesign/ operation in a SPARC M5-32/M6-32 server?

**A.** There are two clock boards in a redundant configuration, so the failure of the primary clock board does not cause a reboot of the server.
**B.** There are two clock boards. Each has two clock sources, so if a clock source fails, the alternate clock source on the same board takes over.
**C.** While the system is running on an active clock board, a faulty clock board can be replaceddynamically.
**D.** There are two clock boards in a redundant configuration, and there is a dynamic failover of theprimary clock board to the standby clock board.

**Answer: A,B,D**

### Question No : 17

If you have a PDomain with expandable= false, which components will not be tested when the PDom goes through POST?

**A.** CMIs
**B.** IOUs
**C.** SSBs
**D.** DIMMs

**Answer: C**
Reference:

https://docs.oracle.com/cd/E24355_01/html/E41214/bbgebhfe.html

**Question No : 18**

You enter the following command from the service processor; start /HOSTO/console.What does this accomplish?

**A.** Log in to the domain as user root via the console.
**B.** Log In to the host as user root from another system.
**C.** Confirm that the boot disk configuration is as expected.
**D.** Connect to the console from the ACTIVE SP.

**Answer: D**
Reference:

https://docs.oracle.com/cd/E19720-01/820-1188-12/core_ilom_appa.html

**Question No : 19**

Which two statements are true according to Enterprise Installation Stan (EIS) regarding boot disk layout?

**A.** For ZFS-based root file systems, EIS recommends you do not use H/W RAID 1 unless sufficient disks are available for ZFS to mirror itself.
**B.** For ZFS-based root file systems, EIS recommends you do not use H/W RAID 0 unless sufficientdisks are available for ZFS to mirror itself.
**C.** Under ZFS, the boot devices/disks are placed in a ZFS poof (called rpool by default), and all datasets (consisting of file systems and volumes) are allocated flexibly from that pool.
**D.** For UFS-based root file systems, EIS recommends that H/W RAID 0 be used to mirror the bootdisk wherever it is available.

**Answer: A,C**

**Question No : 20**

Which three statements are true about the SPARC M5-32 server power grid connection?

**A.** The server is designed to be powered by two utility power grids.